

LIQUID CRYSTAL DISPLAY DEVICE AND PROJECTION TYPE DISPLAY
APPARATUS

TECHNICAL FIELD

[0001] The present invention relates to a liquid crystal display device in which a liquid crystal layer is sandwiched between a pair of substrates bonded to each other by a sealing material so that alignment films face each other across a predetermined gap and a projection type display apparatus using this liquid crystal display device.

BACKGROUND ART

[0002] A liquid crystal projector or other projection type display apparatus separates light emitted from a light source into red, green, and blue, modulates the colored light by three light valves each constituted by a liquid crystal display device (hereinafter referred to as an "LCD"), combines the modulated colored lights again, and enlarges and projects the same onto a projection screen.

[0003] As a light valve mounted in a liquid crystal projector etc., generally use is made of an active matrix drive type LCD using a thin film transistor (hereinafter referred to as a "TFT") drive.

[0004] Almost all active matrix drive type LCDs use nematic liquid crystal. As the main

display system, there is an optical rotation mode LCD.

[0005] The nematic liquid crystal used in an optical rotation mode LCD is a twisted nematic (TN type) liquid crystal having a molecular orientation twisted by 90 degrees and in principle gives a high contrast ratio and a good gray-scale display property in monochrome display.

[0006] For uniform display of active matrix drive type LCDs, it is necessary to uniformly orient the liquid crystal molecules on the entire substrate surface.

[0007] In substrates formed with two electrodes formed with alignment films, the alignment films of the substrates are arranged facing each other. In the seal region located around a pixel display area in which an image is actually displayed, the substrates are bonded to each other by a sealing material. In order to control the gap between the substrates, use is made of spherical spacers called "micro pearls" before the above bonding or use is made of a columnar spacers formed by a resist.

[0008] By going through these processes, empty cells are produced. Thereafter, a liquid crystal is sealed in the empty cells whereby liquid crystal cells are formed.

[0009] Note that the above liquid crystal is comprised of several types of single liquid crystal materials and is also called a "liquid crystal composition". Polarizers are attached to the produced liquid crystal cells whereby the liquid crystal display device is

completed.

[0010] In these liquid crystal panels, at the time of the formation of the seal, if portions of the alignment films and the seal are superimposed on each other, the shielding property is lowered, the seal peels off, and other problems arise or moisture invades the liquid crystal through the interface between the alignment films and the seal to cause deterioration of the liquid crystal and other problems. Particularly when forming the films by spin coating, the films are formed up to the ends of the substrates, so the problems occur in a remarkable manner.

[0011] Further, in a projection type LCD used in a projector, since the image is projected enlarged, abnormalities in image quality easily stand out. Further, the amount of the light striking the panel is very large in comparison with a direct view type, therefore the panel becomes a high temperature, and deterioration due to entry of a minute amount of moisture is again easily seen.

[0012] In order to solve these problems, for example, the method of stopping the entry of moisture by adding a filling agent called a "filler" into the sealing material has generally been used (see for example, Patent Document 1 and Patent Document 2).

[0013] In the LCD of Patent Document 1, a filler with a mean particle size of about 2 μm is added for the purpose of adjusting the viscosity of the seal.

[0014] Further, in the LCD of Patent Document 2, conductive beads of 6.0 μm or 6.5 μm are mixed in as conductive materials, and a conductive filler having a mean particle size of 0.1 to 0.5 μm is added in order to obtain conduction between facing electrodes via the conductive beads.

[0015] Patent Document 1: Japanese Patent Publication No. 11-15005

[0016] Patent Document 2: Japanese Patent Publication No. 11-95232

DISCLOSURE OF THE INVENTION

Problems to be Solved by the Invention

[0017] The active matrix drive type LCDs used as the light valves of projection type display apparatuses are being made smaller in size along with the reduction in size of liquid crystal projectors and other projection type display apparatuses. On the other hand, they are being made higher in pixel definition and higher in luminance. Along with the higher definitions, the pitch between the pixels of liquid crystal display devices has become smaller.

[0018] For example, in a case of an XGA (extended Graphics Array) type having a substrate size of 22.9 mm (0.9 inch), the number of pixels is 1024.times.768, so the pixel pitch becomes 18 μm .

[0019] Since devices are becoming higher in definition, the intervals between transparent electrodes (for, example, ITO: Indium Tin Oxide) of the pixels are becoming increasingly narrow. In a case where potentials at the pixels are inverted, an electric field is generated in a lateral direction.

[0020] Due to the generation of this lateral direction electric field, the orientation of the liquid crystal molecules at the boundaries of the transparent electrodes is disturbed, a borderline is generated at the boundary between the disturbed portion and a normal portion (hereinafter also referred to as a "discrimination line") as a display defect, and the contrast falls. Further, along with the higher luminance, this display defect becomes remarkable.

[0021] Accordingly, in a matrix type liquid crystal display device provided with TFTs (thin film transistors) and other switching elements, it is difficult to achieve both a high aperture ratio and a high contrast ratio.

[0022] Further, in the LCD disclosed in Patent Document 1 or 2, when using a filler having a mean particle size of about 2 μm or mixing in conductive beads of 6.0 μm or 6.5 μm and using a conductive filler having a mean particle size of 0.1 to 0.5 μm , in a projector or other liquid crystal panel having a narrow gap tending to narrow the cell gap, there is the disadvantage that a gap defect occurs, so poor reliability frequently occurs.

[0023] An object of the present invention is to provide a high quality liquid crystal display device able to prevent deterioration, etc. of a liquid crystal panel even if the liquid crystal panel used for the light valve of a projector, etc. is operated under a high temperature and high humidity environment, and a projection type display apparatus using that liquid crystal display device.

MEANS FOR SOLVING THE PROBLEMS

[0024] To achieve the above object, a first aspect of the present invention provides a liquid crystal display device having two substrates on which alignment films for orienting liquid crystal in a predetermined direction are formed, the alignment films facing each other across a predetermined gap by using a sealing material to bond the pair of substrates between which a liquid crystal layer is sandwiched, wherein the sealing material contains a filler having a mean particle size of less than 0.5 μm , the liquid crystal material used in the liquid crystal layer has a refractive index anisotropy at room temperature of 0.16 or more, and a cell gap is 3 μm or less.

[0025] Preferably, the content of the filler contained in the sealing material is within a range of 15 to 40 wt %.

[0026] Preferably, a maximum particle size of the filler contained in the sealing material is 1.5 μm or less.

[0027] Preferably, a specific surface area of the filler contained in the sealing material is $30 \text{ m}^2/\text{g}$ or less.

[0028] Preferably, there is an alignment film under the seal of at least one substrate.

[0029] Further, preferably, the alignment film material is an inorganic alignment film.

[0030] A second aspect of the present invention provides a projection type display apparatus comprising a light source, a condensing optical system for guiding the light emitted from the light source to a liquid crystal display device, and a projection optical system for enlarging and projecting light modulated by the liquid crystal display device, wherein the liquid crystal display device has two substrates on which alignment films for orienting liquid crystal in a predetermined direction are formed, the alignment films facing each other across a predetermined gap by using a sealing material to bond the pair of substrates between which a liquid crystal layer is sandwiched, the sealing material contains a filler having a mean particle size of less than $0.5 \text{ }\mu\text{m}$, the liquid crystal material used in the liquid crystal layer has a refractive index anisotropy at room temperature of 0.16 or more, and a cell gap is $3 \text{ }\mu\text{m}$ or less.

[0031] According to the present invention, for example, the smaller the size of the filler used for the purpose of the reliability against entry of moisture, the more effective. According to experiments, unless using a filler having a mean particle size of less than $0.5 \text{ }\mu\text{m}$, desirably $0.3 \text{ }\mu\text{m}$, poor reliability occurring due to entry of moisture frequently

occurs, and no effect is seen at all.

[0032] Further, if the content of the filler is less than 15 wt %, no effect is seen at all against poor reliability due to the entry of moisture, while when larger than 40 wt %, the viscosity ends up rising, and the work efficiency is degraded.

[0033] Further, as the grounds for why the maximum particle size of the filler contained in the sealing material is 1.5 μm or less, if using a filler in which particles having a large size are mixed, in a projector of another liquid crystal panel having a narrow gap of a cell gap of 3 μm or less, gap defects will end up occurring. Further, the specific surface area is the surface area per unit weight, but when it becomes too large, the ratio of minute particles becomes large, and the viscosity of the sealing material abruptly rises.

According to experiments, if the specific surface area is 30 m^2/g or less, a sealing material having no problems in work efficiency can be obtained. The shape of these fillers is preferably spherical. The reason for this is that the specific surface area of a sphere is the smallest and the rise in viscosity is small when it is mixed into a sealing material.

[0034] Further, when the alignment films and a portion of the seal are superimposed, the shielding property is lowered and various other problems occur. In recent years, spin coating has been used for forming the alignment films for the purpose of shortening the tact time and reducing the material used and thereby lowering the cost, but in this case, the films are formed up to the ends of the substrates, therefore the seal and the alignment

films end up being completely superimposed over each other, so the drop in the reliability becomes a big problem. The present invention is very effective, particularly for a liquid crystal display device employing alignment films formed by spin coating.

[0035] Further, the liquid crystal material used in the liquid crystal layer is characterized in that the refractive index anisotropy at room temperature is 0.16 or more and the cell gap is 3 μm or less.

[0036] As devices become higher in definition and the pixels become smaller in size, the cell gaps will probably become increasingly narrow. In particular, fine filler of less than a 0.5 μm size will probably be very effective.

[0037] In a projection type LCD, since images are projected enlarged, abnormalities in image quality tend to appear more conspicuously. Deterioration due to the panel becoming high in temperature and minute amounts of moisture entering also tends to be easily seen. The present invention is extremely effective particularly, for projection type liquid crystal display devices.

EFFECT OF THE PRESENT INVENTION

[0038] According to the present invention, there are the advantages that a higher image quality can be realized by an improvement of the moisture resistance and a higher contrast can be realized by the higher refractive index anisotropy liquid crystal and the

narrower cell gap along with higher definition.

[0039] Further, ~~the~~a reduction of the seal width becomes possible, and a higher aperture ratio can be realized by the smaller size of the panel or larger valid pixel area.

[0040] Further, in a projector or other projection type LCD, there are the advantages that a higher luminance can be realized by the possible higher illumination of the lamp, and that an improvement of the work efficiency, a higher productivity due to the prevention of abnormalities in the cell gap, and a higher yield can be realized. Further, there is the advantage that a response speed can be improved by narrowing the cell gap. This is useful for moving picture characteristics.

BRIEF DESCRIPTION OF THE DRAWINGS

[0041] FIG. 1 is a sectional view showing the schematic configuration of an active matrix type liquid crystal display device according to the present invention.

[0042] FIG. 2 is a diagram showing the relationship between a cell gap and contrast.

[0043] FIG. 3A and FIG. 3B are views for explaining the relationship between a cell gap d and a refractive index anisotropy Δn .

[0044] FIG. 4 is a diagram showing the relationship between retardation (Δnd) and

transmittance;

[0045] FIG. 5 is a schematic view of a pixel portion of an active matrix type liquid crystal display device according to the present embodiment.

[0046] FIG. 6 is an example of a sectional view of an active matrix type liquid crystal display device of the present embodiment.

[0047] FIG. 7A to FIG. 7F are diagrams for explaining processes of a cell fabricated in the present embodiment (production processes of the liquid crystal panel).

[0048] FIG. 8 is a schematic view of the configuration showing an example of an electronic apparatus using the liquid crystal display device according to the present embodiment as constituted by a projection type display apparatus.

[0049] FIG. 9 is a schematic view of the configuration showing another example of an electronic apparatus using the liquid crystal display device according to the present embodiment as constituted by a projection type display apparatus.

DESCRIPTION OF NOTATIONS

[0050] 10 . . . liquid crystal display device; 11 . . . TFT array substrate; 12 . . . counter substrate; 13 . . . pixel electrode; 14 . . . counter electrode; 15 . . . sealing material;

16 . . . liquid crystal layer; 20, 21 . . . alignment films; 300, 500 . . . projection type display apparatuses; 301, 520 . . . light sources; 303, 506 . . . projection optical systems; 310, 600 . . . projection screens.

BEST MODE FOR WORKING THE INVENTION

[0051] Below, embodiments of the present invention will be explained with reference to the drawings.

[0052] FIG. 1 is a sectional showing the schematic configuration of an active matrix type liquid crystal display device according to the present invention.

[0053] A liquid crystal display device 10 according to the present embodiment is provided with, as shown in FIG. 1, two substrates arranged so as to face each other, that is, a TFT array substrate 11 and a transparent counter substrate 12 arranged so as to face the TFT array substrate 11.

[0054] The TFT array substrate 11 is made of, for example, a quartz substrate, while the counter substrate 12 is made of, for example, a glass substrate or a quartz substrate. The TFT array substrate 11 is provided with pixel electrodes 13 and is made of a transparent conductive thin film, such as ITO film (Indium Tin Oxide). The counter substrate 12 is formed with an ITO film 14 on the entire surface on the side facing the TFT array substrate 11.

[0055] The TFT array substrate 11 and the counter substrate 12, as will be explained later, are formed with not shown alignment films for orienting the liquid crystal in predetermined directions. The alignment films are made to face each other across a predetermined gap by using a sealing material 15 to bond the pair of substrates between which a liquid crystal layer 16 is sandwiched (sealed).

[0056] Further, in the liquid crystal display device 10 according to the present embodiment, the sealing material 15 contains a nonconductive filler having a mean particle size of less than 0.5 μm .

[0057] Further, in the liquid crystal display device 10 according to the present embodiment, the refractive index anisotropy Δn of the liquid crystal material used in the liquid crystal layer 16 at room temperature is set at 0.16 or more, for example, 0.16, 0.17, 0.18, or 0.20, and the interval between the TFT array substrate 11 and the counter substrate 12 (in actuality, the interval between one alignment film and another alignment film), that is, the cell gap d , is set at 3 μm or less.

[0058] Below, a further detailed explanation will be given of the characteristic features of the liquid crystal display device 10 according to the present embodiment, that is, the refractive index anisotropy Δn and the cell gap d of the liquid crystal material and the set value of the filler contained in the sealing material 15.

[0059] A liquid crystal display device 10 having such a configuration is used as, for example, a light valve of a projection type display apparatus.

[0060] Liquid crystal display devices 10 used as the light valves of liquid crystal projectors or the like are being made smaller in size along with the reduction in size of projection type display apparatuses and are being made higher in definition, such as with a pixel pitch of 20 μm or less, for example, 18 μm in an XGA type.

[0061] In this way, in a liquid crystal projector or other device with a narrow pitch of 20 μm or less, there is a problem of a reverse tilt domain (RDT) due to the lateral electric field. As a countermeasure for this, it is effective to narrow the gap, that is, make the cell gap smaller, to strengthen the electric field in the vertical direction of the TFT array substrate 11 and the counter substrate 12 and prevent the influence of the electric field in the lateral direction.

[0062] FIG. 2 is a diagram showing the relationship between the cell gap and contrast.

[0063] In FIG. 2, the abscissa indicates the cell gap d , and the ordinate indicates the relative contrast ratio. Further, the measurement was carried out by using a monochrome display 3-plate type projector in a dark room under the conditions of one point at the center of the panels.

[0064] As shown in FIG. 2, in order to satisfy the relative contrast ratio of 1 or more, the

cell gap d is desirably $3\text{ }\mu\text{m}$ or less.

[0065] Namely, when ~~the~~ narrowing the gap, the effective applied voltage rises, so it becomes possible to obtain a high contrast ratio and it also becomes possible to realize an improvement of the response speed.

[0066] Next, consider the relationship between the cell gap d and the refractive index anisotropy Δn .

[0067] FIG. 3A and FIG. 3B are views for explaining the relationship between the cell gap d and the refractive index anisotropy Δn .

[0068] In FIG. 3A and FIG. 3B, PL indicates a polarizer, DL indicates a analyzer, GL1 and GL2 indicate glass substrates, and LCM indicates a liquid crystal molecule.

[0069] In a light valve using the liquid crystal display device 10, as shown in FIG. 3A and FIG. 3B, when the polarizer PL and the analyzer DL are arranged perpendicularly to each other and in the case of a normally white (NW) mode where white is displayed when turned off, Δn giving a high transmittance is determined by the following Gooch-Tarry formula:

[Formula 1]

Gooch-Tarry formula

$$T = 1 - \frac{\sin^2 \left(\left(1 + \left(2 \Delta n d / \lambda \right)^2 \right)^{1/2} \pi / 2 \right)}{1 + \left(2 \Delta n d / \lambda \right)^2}$$

[0070] In other words, the transmittance when the voltage is OFF depends upon the wavelength of the light and the retardation ($\Delta n d$), and the relationship of the above Gooch-Tarry formula stands.

[0071] FIG. 4 is a diagram showing the relationship between the retardation ($\Delta n d$) and the transmittance.

[0072] In FIG. 4, the abscissa indicates the retardation ($\Delta n d$), and the ordinate indicates the transmittance.

[0073] Further, in FIG. 4, a curve indicated by A shows the characteristic of blue light having a wavelength of 450 nm, a curve indicated by B shows the characteristic of green light having a wavelength of 550 nm, and a curve indicated by C shows the characteristic of red light having a wavelength of 650 nm.

[0074] When designing a cell, use is made of the $\Delta n d$ of the maximum value of the transmittance of the green light based on the above Gooch-Tarry Formula. In the example of FIG. 4, there are two maximum values of the transmittance of the green light with respect to the retardation ($\Delta n d$), that is, 0.48 μm and 1.07 μm , but usually the first

maximum value $0.48\ \mu\text{m}$ is employed due to the relationship with the response speed.

[0075] Namely, the cell is designed to be able to obtain the maximum transmittance when the refractive index anisotropy Δn of the liquid crystal material x cell gap $d=0.48\ \mu\text{m}$. (480 nm).

[0076] In order to obtain this maximum transmittance, when applying a countermeasure, such as reduction of the cell gap explained above, it is necessary to make the refractive index anisotropy Δn of the liquid crystal higher.

[0077] Namely, in order to realize a narrow cell gap d , it becomes necessary to make the Δn of the liquid crystal higher.

[0078] However, in a liquid crystal, generally, as the refractive index anisotropy Δn becomes higher, the moisture resistance and the heat resistance tend to be degraded, and the selection of the liquid crystal materials tends to become smaller.

[0079] Table 1 shows the relationship between the Δn and display abnormalities in an environment test.

[0080] Table 1 shows results by running an accelerated drive test to a past liquid crystal display device by using an environment tester under an environment of 60°C and 90%.

[TABLE 1]

Δn	Display abnormality				
	0.12	0.14	0.16	0.18	0.2
0h	OK	OK	OK	OK	OK
10h	OK	OK	OK	OK	OK
30h	OK	OK	OK	OK	NG
50h	OK	OK	OK	NG	NG
100h	OK	OK	NG	NG	NG

[0081] As seen from Table 1, when the refractive index anisotropy Δn is 0.16 or more, along with aging, display abnormalities start to occur. When it becomes 0.18 or 0.20, the probability of the occurrence of display abnormalities becomes higher.

[0082] Therefore, in order to suppress the occurrence of display abnormalities, even in the case of this high Δn , the interval between the TFT array substrate 11 and the counter substrate 12, that is, the cell gap d , is set at 3 μm or less, the refractive index anisotropy Δn of the liquid crystal material used in the liquid crystal layer 16 at room temperature is set at 0.16 or more, and further a nonconductive filler having a mean particle size of less

than 0.5 μm is contained in (added to) the sealing material 15.

[0083] The liquid crystal display device 10 according to the present embodiment to which a nonconductive filler having a mean particle size of less than 0.5 μm was added in this way was subjected to an accelerated drive test using an environment tester under an environment of 60°C and 90%.

[0084] The results are shown in Table 2.

[TABLE 2]

Δn	Display abnormalities				
	0.12	0.14	0.16	0.18	0.2
0h	OK	OK	OK	OK	OK
10h	OK	OK	OK	OK	OK
30h	OK	OK	OK	OK	OK
50h	OK	OK	OK	OK	OK
100h	OK	OK	OK	OK	OK

[0085] As shown in Table 2, in the liquid crystal display device 10 according to the present embodiment, the occurrence of display abnormalities along with aging is suppressed irrespective of the high Δn .

[0086] Note that, the results of Table 2 are based on specific examples explained later in detail.

[0087] In the case of application of the liquid crystal display device for a projector, light of 20,000,000 LX or more is illuminated, and the operation temperature becomes 60°C or more.

[0088] Accordingly, as shown by the results of Table 2, even when the liquid crystal display device 10 according to the present embodiment is used under actual severe conditions, there is no worry of about the occurrence of display abnormalities along with aging.

[0089] For example, in a high Δn liquid crystal, that is, a tolan system, $\Delta n=0.2$, but according to the liquid crystal display device 10 according to the present embodiment, as shown in Table 2, there is no apprehension of the occurrence of display abnormalities along with aging even when $\Delta n=0.2$, therefore the selection of the liquid crystal materials is broadened, and, as a result, a high Δn material margin can be realized.

[0090] Non-patent Document 1: Monthly Display, January 2002, High Refractive Index

Anisotropy Liquid Crystals, SUMITOMO. CHEMICAL Ltd., Sekine et al.

[0091] As described above, along with the narrowing of the gap, a higher refractive index anisotropy (Δn) is necessary. In order to achieve this, a nonconductive filler having a mean particle size of less than 0.5 μm is added to the sealing material 15.

[0092] Conversely, when a higher Δn can be achieved, as in the present embodiment, a narrowing of the gap can be realized, the reverse tilt domain (RTD) countermeasure can be taken, and it becomes possible to achieve a higher image quality and a higher definition.

[0093] Due to this, the effective application voltage rises, the field intensity rises, and a higher contrast can be achieved.

[0094] With the future increasingly higher definitions and smaller sizes, along with the reduction of the pixel pitch, the influence of the reverse tilt domain (RTD) will become further severe, but the liquid crystal display device 10 according to the present embodiment can sufficiently cope with this. Further, an improvement of the response speed can be realized.

[0095] The filler added to the sealing material 15, for example, the silica, must have little variation in quality, be good in uniformity, and be small in particle size. In the present embodiment, the mean particle size is set at less than 0.5 μm .

[0096] In the present embodiment, the following conditions are set for the filler added to the sealing material 15. Note that, in the following conditions, the reasons (grounds) for selection by the plurality of examples explained in detail later are clearly proved.

[0097] Namely, the content of the filler contained in the sealing material 15 is within the range of 15 to 40 wt %.

[0098] Further, the maximum particle size of the filler contained in the sealing material 15 is 1.5 μm or less.

[0099] Further, the specific surface area of the filler contained in the sealing material 15 is 30 m^2/g or less.

[0100] Further, the TFT array substrate 11 and the counter substrate 12 are formed with alignment films for orienting the liquid crystal in predetermined directions, but in the present embodiment, at least one substrate between the TFT array substrate 11 and the counter substrate 12 has the alignment film formed under the sealing material.

[0101] Here, the mean particle size of the filler may be measured by, for example, a scan type electron microscope (SEM) or may be the cumulative weight mean particle size based on laser scattering. Further, the specific surface area is measured according to the BET method.

[0102] The smaller the diameter of the filler used for the purpose of reliability against entry of moisture, the larger the effect. According to experiments, unless a filler having a mean particle size of less than $0.5\text{ }\mu\text{m}$, desirably $0.3\text{ }\mu\text{m}$ or less, is used, poor reliability frequently occurs due to the entry of moisture. It is seen that the effect is liable to disappear completely.

[0103] Further, the content of the filler is set to within the range of 15 to 40 wt % because if the content of the filler is less than 15 wt %, no effect is seen on the poor reliability occurring due to entry of moisture, while if the content is larger than 40 wt %, the viscosity rises and the work efficiency is degraded. Further, as the grounds for making the maximum particle size of the filler contained in the sealing material $15\text{ }\mu\text{m}$ or less, if a filler containing particles having a large particle size is used, in a liquid crystal panel having a narrow gap, i.e., a cell gap of $3\text{ }\mu\text{m}$ or less, for example, in a projector, gap defects occur.

[0104] Further, the grounds for making the specific surface area of the filler contained in the sealing material $30\text{ m}^2/\text{g}$ or less are as follows.

[0105] The specific surface area is the surface area per unit weight. When it becomes too large, the ratio of fine particles becomes large, and the viscosity of the sealing material abruptly rises. According to experiments, when the specific surface area was $30\text{ m}^2/\text{g}$ or less, a sealing material having no problem in work efficiency could be obtained.

[0106] The shape of the filler particles is preferably spherical. The reasons for this are the smallest specific surface area of spheres and the small rise in viscosity when mixed into a sealing material.

[0107] Further, in the present embodiment, the reason for having at least one substrate between the TFT array substrate 11 and the counter substrate 12 be formed with an alignment film under the sealing material is as follows.

[0108] When an alignment film and a portion of the sealing material are superimposed on each other, the shielding property is lowered and various other problems occur. In recent years, spin coating has been employed for forming the alignment films for shortening the tact time and reducing the materials and thereby lowering the costs. In this case, the film is formed up to the ends of the substrate. Therefore, the seal and the alignment film are completely superimposed on each other. The drop in reliability has become a big problem.

[0109] The present embodiment is extremely effective particularly for a liquid crystal display employing alignment films formed by the spin coating.

[0110] Further, the alignment films are inorganic alignment films.

[0111] As inorganic alignment films, there can be mentioned silicon oxide, etc. formed

by vapor deposition. Also, oxides $\text{CaF}_{2.2}$, $\text{MgF}_{2.2}$, etc. able to be vapor deposited are possible.

[0112] Other than these, materials having a siloxane skeleton formed by printing, spin coating, or ink jet can be mentioned.

[0113] A more concrete configuration of the liquid crystal display device 10 having the above configuration will be explained with reference to FIG. 5 and FIG. 6.

[0114] FIG. 5 is a schematic view of a pixel portion of an active matrix type liquid crystal display device according to the present embodiment, while FIG. 6 is an example of a sectional view of an active matrix type liquid crystal display device of the present embodiment.

[0115] The liquid crystal display device 10 is provided with, as explained with reference to FIG. 1, a TFT array substrate 11 and a transparent counter substrate 12 arranged so as to face this. The TFT array substrate 11 is made of, for example, a quartz substrate, while the counter substrate 12 is made of, for example, a glass substrate or a quartz substrate. The TFT array substrate 11 is provided with pixel electrodes 13 and is made of a transparent conductive thin film, such as an ITO film (Indium Tin Oxide film). The counter substrate 12 is provided with the ITO film (counter electrode) 14 explained above over its entire surface. The counter substrate 12 is further provided with a light shielding film 17 in a region other than the opening region of each pixel portion.

[0116] The pixel electrode 13 and a protective film 19 and the counter electrode 14 facing this are formed with alignment films 20 and 21 for orienting the liquid crystal 16 in predetermined directions at the time of the production of the liquid crystal panel.

[0117] Here, a simple explanation will be given of the configuration of the TFT array substrate 11.

[0118] Each of the plurality of pixel electrodes 13 formed in a matrix for forming the image display area of the liquid crystal display device 10 is provided with a pixel switching use TFT 22 for controlling the pixel electrode 13 at an adjacent position.

[0119] A signal line 23 to which a pixel signal is supplied is electrically connected to a source 24 of the TFT 22 explained before. It supplies the pixel signal to be written to the signal line 23.

[0120] Further, a scanning line 25 is electrically connected to a gate of the TFT 22 and applies a scanning signal to the scanning line 25 in the form of a pulse at a predetermined timing.

[0121] The pixel electrode 13 is electrically connected to a drain 26 of the TFT 22. By making the switch of the switching element constituted by the TFT 22 conductive for exactly a constant period, the pixel signal supplied from the signal line 23 is written at a

predetermined timing.

[0122] The pixel signal of a predetermined level written in the liquid crystal via the pixel electrode 13 is held for a constant period between the same and the counter electrode (ITO film) 14 formed on the counter substrate 12.

[0123] The liquid crystal layer 16 modulates the light by the change of the orientation and order of molecules set according to the applied voltage level and enables a gray-scale display. In a normally white display, the incident light is allowed to pass through this liquid crystal portion in accordance with the applied voltage, and as a whole, light having a contrast in accordance with the pixel signal is emitted from the liquid crystal display device.

[0124] Here, in order to prevent a held pixel signal from leakage, a storage capacitor 27 is added parallel to a liquid crystal capacitor formed between the pixel electrode 13 and the counter electrode 14. Due to this, the storage characteristic is further enhanced, and a liquid crystal display device having a high contrast ratio can be realized. Further, in order to form such a storage capacitor 27, a Cs line 28 given a low resistance is provided. Note that, 29 indicates an inter-layer insulation film, and 30 indicates a semiconductor layer.

[0125] Then, a transparent resist layer acting as a not shown columnar spacer is formed with respect to the counter substrate 12 explained above.

[0126] The substrate is coated with a photoresist comprised of PMER (made by TOKYO OHKA KOGYO CO., LTD.) by spin coating to a thickness of 3 μm , then is exposed by UV-rays using a photo-mask, and then is developed, to thereby form a not shown columnar spacer.

[0127] Below, an explanation will be given of the process of bonding the TFT array substrate 11 and the counter substrate 12 having the above configurations by the sealing material 15 and the device characteristics as specific Examples 1 to ~~Example~~ 5.

[EXAMPLE 1]

[0128] Next, an explanation will be given of the process of fabrication of a cell in the present embodiment (process of production of liquid crystal panel) with reference to FIG. 7A to FIG. 7F.

[0129] First, as shown in FIG. 7A, the TFT array substrate 11 and the counter substrate 12 were washed by using a neutral detergent or pure water, and then were dried at 120°C for 20 minutes. As the materials of the substrates, use was made of quartz substrates for both.

[0130] Then, as shown in FIG. 7B, each substrate was formed with an alignment film. An alignment film made of polyimide was coated to a thickness of about 50 nm by spin coating; and then as-dried at 100°C for 1 minute (temporary firing). As the polyimide, use

was made of, for example, soluble polyimide (made by Japan Synthetic Rubber Co., Ltd.) (spinner used at 2000 rmp for 30 seconds).

[0131] Note that, in the present embodiment, spin coating was used for the formation of the alignment films, but printing may be used or a ink jet may be used as well. Further, the type of the alignment film material is not limited to an organic material such as polyimide. An inorganic material, such as silicon, may also be used. In the case of an inorganic material, vapor deposition may be used in the formation method.

[0132] Post baking was carried out at 180°C for 1 hour to dry off the solvent.

[0133] Thereafter, as shown in FIG. 7C, ~~the~~ rubbing was carried out. The rubbing was carried out by using a rayon fabric at a rubbing angle of 90° and under a condition of two rubbing operations.

[0134] After the rubbing, the film was washed by water in order to remove dust of the fabric used in the rubbing and shaved PI.

[0135] Next, a seal pattern ~~formed~~ excluding an injection port was formed.

[0136] As examples of the sealing materials used in this example, sealing materials containing as principal components epoxy resin plus silica fillers (hereinafter referred to as the "filler") and not plus any fillers were used.

[0137] As examples of the fillers, melted silica, crystal silica, alumina, silicon nitride, etc. are preferred. Different types of fillers may also be mixed.

[0138] As the fillers, spherical silicas having mean particle sizes of four conditions of 0.3 μm , 0.5 μm , 1 μm , and 2 μm , having specific surface areas set at 20 m^2/g , having contents of 20 wt %, and having maximum particle sizes of 1.0 μm or less were used.

[0139] The mean particle size was measured by SEM by sampling 100 particles, the specific surface area was measured by the BET method, and the content was measured by an electronic balance. As the maximum particle size, the particle having the maximum size was confirmed by a magnification of 30,000. The viscosities of these sealing materials were about 200,000 mPas as a result of the measurement at room temperature by a cone plate type viscometer.

[0140] As shown in FIG. 7D, each prepared sealing material was dispensed by a dispenser over the periphery of a counter substrate in a pattern excluding the injection port. As shown in FIG. 7E, the TFT array substrate 1 and the counter substrate 2 were then superimposed on each other, and the cell gap was set at 3.0 μm . The seal width was about 0.7 mm.

[0141] Thereafter, as shown in FIG. 7F, the liquid crystal material ($\Delta n=0.16$) was filled, and the injection port was sealed (not shown) by an UV-ray curable resin.

[0142] As the liquid crystal material, use was made of one obtained by newly adding a minute amount of a monomer having a high refractive index anisotropy Δn .

[0143] Each liquid crystal display device of the present example was subjected to an accelerated drive test using an environment tester under an environment of 60°C and 90%. The results are shown in Table 3.

[TABLE 3]

Situation of Occurrence of Display Abnormalities After 50 Hours of high temperature high humidity test (60°C, 90%)

(Example 1, Example 2)

Content	Mean particle size (μm)			
	0.3	0.5	1	2
20	OK	OK	NG	NG
10	NG			
15	OK			
30	OK			
40	OK			
45	Viscosity rises			

← Ex. 1

↑ Ex.2

[0144] When observed after the elapse of 50 hours, display abnormalities due to the entry of moisture occurred when there was no filler and the mean particle size was 1.0 μm and 2.0 μm . The tendency became greater along with the elapse of time, but when the mean particle size was 0.5 μm and 0.3 μm , no display abnormalities occurred even after driving for 100 hours.

[0145] In this way, by using the liquid crystal display of this example, a liquid crystal display device having a higher reliability and a higher quality can be obtained.

(EXAMPLE 2) Relationship Between Filler Content and Reliability and Display Abnormalities

[0146] Up to the rubbing, each display was prepared in the same way as Example 1, and then a seal pattern excluding the injection port was formed.

[0147] As examples of the sealing materials used in this example, use was made of sealing materials containing an epoxy resin as a principal component plus silica fillers (hereinafter, referred to as the "filler").

[0148] As fillers, spherical silicas having mean particle sizes of 0.3 μm , specific surface areas of 20 m^2/g , contents of six conditions of 10 wt %, 15 wt %, 20 wt %, 30 wt %, 40 wt %, and 45 wt %, and maximum particle sizes of 1.0 μm or less were used.

[0149] The mean particle size was measured by SEM by sampling 100 particles, the specific surface area was measured by the BET method, and the content was measured by an electronic balance. As the maximum particle size, the particle having the maximum size was confirmed by a magnification of 30,000.

[0150] Each prepared sealing material was dispensed by a dispenser over the periphery of a counter substrate in a pattern excluding the injection port, the TFT array substrate 11 and the counter substrate 12 were then superimposed on each other, and the cell gap was set at 3.0 μm . The seal width was about 0.7 mm.

[0151] Thereafter, the liquid crystal material ($\Delta n=0.16$) was filled, and the injection port was sealed (not shown) by an UV-ray curable resin.

[0152] Each liquid crystal display device of this example was observed. The element having the content of 45 wt % was poor in the work efficiency, and the seal was broken due to the rise of the viscosity.

[0153] Next, each liquid crystal display device of this example was subjected to an accelerated drive test using an environment tester under an environment of 60°C and 90%.

[0154] The results are shown in Table 3. When observing the displays after an elapse of 50 hours, display abnormalities due to entry of moisture occurred when the content was

10 wt %. They did not occur for the display having the content of 15 wt % or more. The tendency became greater along with the elapse of time, but no display abnormalities occurred even after driving e-for 100 hours in the display having the content of 15 wt % or more.

[0155] In this way, by using the liquid crystal display device of this example, a higher reliability ~~le~~ and higher quality liquid crystal display device can be obtained.

(EXAMPLE 3) Maximum Particle Size and Gap Abnormalities

[0156] Up to the rubbing, each display was prepared in the same way as Example 1, and then a seal pattern excluding the injection port was formed.

[0157] As examples of the sealing materials used in this example, use was made of sealing materials containing an epoxy resin as a principal component plus silica fillers (hereinafter, referred to as the "filler"). As the fillers, spherical silicas having mean particle sizes of 0.3 μm , specific surface areas of 20 m^2/g , contents of 20 wt %, and maximum particle sizes of the five conditions of 0.5 μm , 1.0 μm , 1.5 μm , 2.0 μm , and 3.0 μm were used.

[0158] The mean particle size was measured by SEM by sampling 100 particles, the specific surface area was measured by the BET method, and the content was measured by an electronic balance. As the maximum particle size, the particle having the maximum

size was confirmed by a magnification of 30,000.

[0159] Each prepared sealing material was dispensed by a dispenser over the periphery of a counter substrate in a pattern excluding the injection port, the TFT array substrate 1 and the counter substrate 2 were then superimposed on each other, and the cell gap was set at 3.0 μm . The seal width was about 0.7 mm.

[0160] Thereafter, the liquid crystal material ($\Delta n=0.16$) was filled, and the injection port was sealed (not shown) by an UV-ray curable resin.

[0161] The liquid crystal display devices of this example were observed. The results are shown in Table 4.

[TABLE 4]

Results of Examination of Gap Abnormalities

(Example 3)

Maximum particle size (μm)	0.5 or less	1.0 or less	1.5 or less	2.0 or less	3.0 or less
Gap Abnormality	OK	OK	OK	NG	NG

[0162] Cell gap abnormalities were observed in the display devices using fillers having maximum particle sizes of 2.0 μm or less and 3.0 μm or less. No cell gap abnormalities were seen in display devices having maximum particle sizes of 0.5 μm or less, 1.0 μm or less, and 1.5 μm or less.

[0163] In this way, by using the liquid crystal display device of this example, a higher reliable and higher quality liquid crystal display can be obtained.

(Example 4) Specific Surface Area and Work Efficiency

[0164] Up to the rubbing, each display was prepared in the same way as Example 1, and then a seal pattern excluding the injection port was formed.

[0165] As examples of the sealing materials used in this example, use was made of sealing materials containing an epoxy resin as a principal component plus silica fillers (hereinafter, referred to as the "filler"). As the fillers, spherical silicas having mean particle sizes of 0.3 μm , specific surface areas of the three conditions of 40 m^2/g , 30 m^2/g , and 20 m^2/g , contents of 20 wt %, and maximum particle sizes of 1.0 μm or less were used.

[0166] The mean particle size was measured by SEM by sampling 100 particles, the specific surface area was measured by the BET method, and the content was measured by an electronic balance. As the maximum particle size, the particle having the maximum

size was confirmed by a magnification of 30,000.

[0167] Each prepared sealing material was dispensed by a dispenser over the periphery of a counter substrate in a pattern excluding the injection port, and then ~~the~~ work efficiency was evaluated.

[0168] The work efficiency was poor when the specific surface area was $40 \text{ m}^2/\text{g}$, and the seal broke due to the rise of the viscosity. The coating could be carried out without problem when the specific surface area was $30 \text{ m}^2/\text{g}$ and $20 \text{ m}^2/\text{g}$.

[0169] In this way, by using the liquid crystal display device of this example, a higher reliability ~~le~~ and higher quality liquid crystal display device can be obtained.

(Example 5) Use of Liquid Crystals of $\Delta n=0.18$ and 0.20

[0170] Up to the rubbing, each display was prepared in the same way as Example 1, and then a seal pattern excluding the injection port was formed.

[0171] As examples of the sealing materials used in this example, use was made of sealing materials containing an epoxy resin as a principal component plus silica fillers (hereinafter, referred to as the "filler") and not plus silica fillers. As the fillers, spherical silicas having mean particle sizes of $0.3 \text{ }\mu\text{m}$, specific surface areas of $20 \text{ m}^2/\text{g}$, contents of 20 wt %, and maximum particle sizes of $1.0 \text{ }\mu\text{m}$ or less were used.

[0172] The mean particle size was measured by SEM by sampling 100 particles, the specific surface area was measured by the BET method, and the content was measured by an electronic balance. As the maximum particle size, the particle having the maximum size was confirmed by a magnification of 30,000.

[0173] Each prepared sealing material was dispensed by a dispenser over the periphery of a counter substrate in a pattern excluding the injection port, the TFT array substrate 1 and the counter substrate 2 were then superimposed on each other, and the cell gap was set at 2.65 μm and 2.4 μm . The seal width was about 0.7 mm.

[0174] Thereafter, two types of liquid crystal materials ($\Delta=0.18, 0.20$) were injected. The liquid crystal material having $\Delta n=0.18$ was injected in a display having a cell gap of 2.65 μm , while the liquid crystal material having $\Delta=0.20$ was injected in a display having a cell gap of 2.4 μm , and then the injection port was sealed (not shown) by a UV-ray curable resin. Note that the ratio of the liquid crystal monomer raising the Δn occupied in the entire liquid crystal composition became larger in a sequence of $0.16 < 0.18 < 0.20$.

[0175] Each liquid crystal display devices of this example was subjected to an accelerated drive test using an environment tester under an environment of 60°C and 90%. The results are shown in Table 1 and Table 2 explained above.

[0176] The display devices were observed after the elapse of 10 hours, whereupon the

liquid crystal material without a filler (conventional example) and having $\Delta n=0.20$ exhibited display abnormalities. The display devices were then observed after the elapse of 30 hours, whereupon the liquid crystal material without a filler (conventional example) and having $\Delta n=0.18$ exhibited display abnormalities. It is considered that the time of occurrence of display abnormalities depends upon the amount of the monomer raising the Δn . Note that this tendency becomes greater along with the elapse of time.

[0177] In the liquid crystal display devices of this example including the fillers, no display abnormalities occurred even after driving e for 100 hours.

[0178] In this way, by using the liquid crystal display device of this example, a higher reliability ~~le~~ and higher quality liquid crystal display device can be obtained.

[0179] Below, an explanation will be given of the configuration of a projection type display apparatus with reference to the schematic view of the configuration of FIG. 9 as an example of an electronic apparatus using a liquid crystal display device having the above characteristic features.

[0180] As shown in FIG. 8, a projection type liquid crystal display apparatus (liquid crystal projector) 300 is constituted by a light source 301, a transmission type liquid crystal display device 302, and a projection optical system 303 arranged along an optical axis C in sequence.

[0181] The light emitted from a lamp 304 constituting the light source 301 is condensed by a reflector 305 to the forward direction in the component radiated backward and made to strike a condenser lens 306. The condenser lens 306 further concentrates the light and guides it to the liquid crystal display device 302 via an incident side polarizer 307.

[0182] The guided light is transformed to an image by the liquid crystal display device 302 having a function of a shutter or light valve and an air polarizer 308. The displayed image is projected enlarged onto a screen 310 via the projection optical system 303.

[0183] Note that a filter 314 is inserted between the light source 301 and the condenser lens 306, and light having a useless wavelength included in the light source, for example, infrared rays and UV-rays, are eliminated.

[0184] Next, an explanation will be given of the configuration of a projection type display apparatus with reference to FIG. 9 as an example of an electronic apparatus using the above liquid crystal display device.

[0185] As a projection type display apparatus 500 shown in FIG. 9, a schematic view of the configuration of an optical system of a projection type display apparatus provided with three liquid crystal display devices explained above and using them as RGB use liquid crystal display devices 562R, 562G, and 562B is shown.

[0186] The projection type display apparatus 500 uses, as the optical system, a light

source device 520 and a uniform illumination optical system 523.

[0187] A color separation optical system 524 as a color separating means for separating light W emitted from this uniform illumination optical system 523 to red (R), green (G), and blue (B), three light valves 525R, 525G, and 525B as modulating means for modulating the colored light R, G, and B, a color combining prism 510 as a color combining means for recombining the modulated colored light, and a projection lens unit 506 as a projecting means for enlarging and projecting the combined light to the surface of the projection screen 600 are provided. Further, a light guide system 527 for guiding the blue light B to the corresponding light valve 525B is provided.

[0188] The uniform illumination optical system 523 is provided with two lens plates 521 and 522 and a reflection mirror 531. The reflection mirror 531 is sandwiched between two lens plates 521 and 522 arranged in a crossing state. Each of the two lens plates 521 and 522 of the uniform illumination optical system 523 is provided with a plurality of rectangular lenses arranged in a matrix.

[0189] The light emitted from the light source device 520 is separated to a plurality of partial lights by the rectangular lenses of the first lens plate 521.

[0190] Then, these partial lights overlap near the three light valves 525R, 525G, and 525B by the rectangular lenses of the second lens plate 522. Accordingly, by using the uniform illumination optical system 523, even in the case where the light source device

520 has a nonuniform illumination distribution in the cross-section of the emitted light; it becomes possible to illuminate the three light valves 525R, 525G, and 525B by uniform illumination light.

[0191] Each color separation optical system 524 is constituted by a blue green reflection dichroic mirror 541, a green reflection dichroic mirror 542, and a reflection mirror 543.

[0192] First, at the blue green reflection dichroic mirror 541, the blue light B and the green light G included in the light W are reflected at a right angle and proceed toward the green reflection dichroic mirror 542. The red light R passes through this blue green reflection dichroic mirror 541, is reflected at the backward reflection mirror 543 at a right angle, and is emitted from an emission portion 544 of the red light R to the prism unit 510 side.

[0193] Next, at the green reflection dichroic mirror 542, between the blue light B and the green light G reflected at the blue green reflection dichroic mirror 541, only the green light G is reflected at a right angle and emitted from an emission portion 545 of the green light G to the color combining optical system side. The blue light B passing through the green reflection dichroic mirror 542 is emitted from an emission portion 546 of the blue light B to the light guide system 527 side.

[0194] Here, the system is set so that the distances from the emission portion of the light W of the uniform illumination optical system 523 to the emission portions 544, 545, and

546 of the colored light in the color separation optical system 524 become substantially equal. On the emission side of the emission portion 544 of the red light R and the emission portion 545 of the green light G of the color separation optical system 524, a condenser lens 551 and a condenser lens 552 are arranged. Accordingly, the red light R and the green light G emitted from the emission portions strike these condenser lens 551 and condenser lens 552 so as to be made parallel.

[0195] The red light R and the green light G made parallel in this way strike the light valve 525R and the light valve 525G where they are modulated and added with image information corresponding to the colors. Namely, these liquid crystal display devices are controlled in response to the image information by a not shown driving means. Due to this, the colored light passing through them ~~are~~is modulated. On the other hand, the blue light B is guided via the light guide system 527 to the corresponding light valve 525B where it is modulated in response to the image information in the same way as the above.

[0196] Note that the light valves 525R, 525G, and 525B of the present example are liquid crystal light valves further including incident side polarizing means 561R, 561G, and 561B and liquid crystal displays 562R, 562G, and 562B arranged between them.

[0197] The light guide system 527 is constituted by a condenser lens 554 arranged on the emission side of the blue light B of the emission portion 546, an incident side reflection mirror 571, an emission side reflection mirror 572, an intermediate lens 573 arranged between these reflection mirrors, and a condenser lens 553 arranged in front of the light

valve 525B.

[0198] The blue light emitted from the condenser lens 546 is guided via the light guide system 527 to the liquid crystal display 562B where it is modulated. Among the light path lengths of the colored light, that is, the distances from the emission portion of the light W to the liquid crystal display devices 562R, 562G, and 562B, the distance to the blue light B becomes the longest. Accordingly, the amount of loss of the blue light becomes the largest.

[0199] However, by interposing the light guide system 527, the loss of light can be suppressed. Color lights R, G, and B modulated by passing through the light valves 525R, 525G, and 525B strike the color combining prism 510 where they are combined.

[0200] Then, the light combined by the color combining prism 510 is enlarged and projected onto the surface of the projection screen 600 existing at a predetermined position via the projection lens unit 506.

[0201] Note that the above effects can be expected even when the present invention is applied ~~to~~ not only to a simple matrix system, but also to any other system of a liquid crystal display device, such as a TFTF active matrix system, a TFD active matrix system, a passive matrix drive system, optical rotation mode, and birefringence mode. Further, the effects can be expected even when the liquid crystal injection device of the present invention is applied ~~to~~ not only to a built-in drive type liquid crystal device, but also to a

type of a liquid crystal display device with an externally attached drive circuit, liquid crystal display devices having a variety of sizes of a diagonal 1 inch to 15 inches or more and projection type liquid crystal display devices.

[0202] Note that the above effects are obtained even when the present invention is applied ~~to~~ not only to a projection type liquid crystal display device, but also any to other system of device such as a reflection type liquid crystal display device, LCOS, and organic EL.

[0203] Further, the above effects can be expected even when the present invention is applied to any system of liquid crystal display device, such as a built-in drive type liquid crystal display device, a type of a liquid crystal display device with an externally attached drive circuit, ~~the~~ a simple matrix system, a TFD active matrix system, a passive matrix drive system, an optical rotation mode, and a birefringence mode.

INDUSTRIAL APPLICABILITY

[0204] The present invention can prevent the deterioration, etc. of a liquid crystal panel even when a liquid crystal panel used in a light valve of a projector or the like is operated under a high temperature and high humidity environment, and therefore it can be applied ~~to~~ not only to a projection type liquid crystal display device, but also to any other system of device, such as a reflection type liquid crystal display device, a LCOS, and an organic EL.